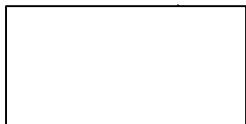Uwe Kindler

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The contribution comprises a port of eCos for Renesas "Evaluation Design O/S Kit for H8S/2674". The port includes.

- H8S architecture port
- H8S/2674 variant port
- EDOSK-2674 platform port
- Generic H8S SCI serial drivers
- EDOSK-2674 serial driver configuration
- EDOSK-2674 network driver configuration for generic LAN SMC91Cxx network driver
- Generic Dallas 1672 wallclock driver
- EDOSK-2674 I2C drivers for Dallas 1672 and wallclock driver configuration
- H8S/2674 watchdog driver
- EDOSK-2674 FLASH driver configuration for generic Intel strata FLASH driver
- Patches for h8300 toolchain

The port is based on the EDOSK-2674 port from Yoshinori Sato (part of the eCos repository) but it is partly completely rewritten, improved, bugfixed and well documented.

	New or completely rewritten files	Modified Files (from Yoshinori Satos EDOSK-2674 port)
H8S arch.	hal_h8s.cdl, hal_arch.h, h8s_stub.h, h8s_sci.h, h8s_sci.c context.S, vectors.S, h8s.ld, h8s_stub.c	hal_intr.h, hal_cache.h, hal_io.h, basetype.h, hal_diag.c, hal_misc.c, hal_syscall.c
H8S/2674 variant	hal_h8s_h8s2674.cdl, var_intr.h, var_intr_numbers.h, variant.inc, var_intr.S, var_misc.c, h8s_except1.cxx, h8s_intr0.cxx, h8s_kexcept1.c, h8s_kintr0.c	mod_regs_adc.h, mod_regs_bsc.h, mod_regs_dmac.h, mod_regs_intc.h, mod_regs_pio.h, mod_regs_ppg.h, mod_regs_sci.h, mod_regs_sys.h, mod_regs_tmr.h, mod_regs_wdt.h, var_arch.h
EDOSK platform	hal_h8s_h8s2674_edosk2674.cdl, plf_io.h, plf_stub.h, delay_us.S, plf_misc.c, knmi.c	hal_diag.h, platform_inc, plf_intr, plf_diag.h
Drivers	edosk2674_eth_drivers.cdl,	watchdog_h8s2674.cdl,

	devs_eth_edosk2674.inl, flash_h8s_edosk2674.cdl, devs_flash_h8s_edosk2674.inl, ser_h8s_edosk2674.cdl, h8s_h8s2674_edosk2674_sci.inl, ser_h8s_sci.cdl, h8s_sci_serial.c, wallclock_ds1672.cdl, ds1672.h, ds1672.cxx, wallclock_edosk2674.cdl, devs_wallclock_h8s_edosk2674.inl, i2c_h8s_edosk2674.c	wtachdog_h8s2674.cxx